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He is the author of four textbooks including Fundamentals of Logic Design 5e. Larry L. Kinney is a Professor and Director of Undergraduate Studies at the University of Minnesota. He received his Ph.D. in Electrical Engineering from the University of Iowa.

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His research has focused on digital system and digital computer design, specifically concurrent error detection techniques, testing of logic and design, distributed computer systems, computer architectures, error detecting/correcting codes and applications of microprocessors.

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A logic circuit realizing the function  $f(A, B, C, D) = \sum(0, 1, 2, 3, 4, 5, 6, 7)$  has four inputs  $A, B, C, D$ . The three inputs  $A, B, C$  are the binary representation of the digits 0 through 7 with  $A$  being the most-significant bit. The input  $D$  is an odd-parity bit, i.e., the value of  $D$  is such that  $A, B, C, D$  always contain an odd number of 1's.

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